

**Electronic Patent Application Fee Transmittal**

<b>Application Number:</b>	10714935			
<b>Filing Date:</b>	18-Nov-2003			
<b>Title of Invention:</b>	Shift register block, and data signal line driving circuit and display device using the same			
<b>First Named Inventor/Applicant Name:</b>	Kazuhiro Maeda			
<b>Filer:</b>	MICHAEL J SHEA			
<b>Attorney Docket Number:</b>	1035-483			
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Independent claims in excess of 3	1201	4	220	880
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				880